

PATENT (5681-67300/P9675)

ED STATES PATENT AND TRADEMARK OFFICE

Applica	tion No.:	10/696,891
Filed:	October 3	30, 2003

Inventor(s):

Robert E. Cypher

ECC FOR COMPONENT FAILURES USING GALOIS FIELDS

Examiner: Unknown Group/Art Unit: 2131

Atty. Dkt. No: 5681-67300

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date indicated below.

> Lawrence J. Merkel Name of Registered Representative

April 21, 2006

INFORMATION DISCLOSURE STATEMENT

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Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:		
	Applic	ant requests consideration of \(\sum \) the references listed on the attached Form PTO-
1449 a	and/or [the additional information identified below in paragraph 3.
		A copy of each required reference listed on the Form PTO-1449 is enclosed.
		The references listed on the Form PTO-1449 were previously cited in application serial no to which the captioned application is seeking priority under 35 U.S.C. §120; therefore, a copy of each reference is not enclosed.
		The present application is a U.S. national patent application filed after June 30, 2003, or an international application that entered the national stage under 35 USC § 371 after June 30, 2003. Therefore, copies of the cited U.S. patents and/or U.S. patent application publications are not enclosed.
1.	This Ir	nformation Disclosure Statement is submitted:
	a.	within 3 months of the filing date of a national application other than a continued prosecution application under § 1.53(d);

			within 3 months of the date of entry of the national stage as set forth in § 1.491 in an International application; before the mailing date of a first Office Action on the merits; or before the mailing of a first Office Action after the filing of a request for continued examination under § 1.114. after the events of above paragraph 1a and prior to the mailing date of a final Office Action or Notice of Allowance, and thus: the certification of paragraph 2 below is provided, or a fee of \$180.00 is enclosed.				
b.	b.						
	c.		after the mailing date of a final Office Action or a Notice of Allowance and prior to payment of the issue fee, and thus: the certification of paragraph 2 below is provided and a fee of \$180.00 is enclosed.				
2.	It is her	reby cert	tified:				
		was cir applica designa	ch item of information contained in this Information Disclosure Statement ted in a communication from a foreign patent office in a counterpart ation and that this communication was not received by any individual ated in § 1.56(c) more than thirty (30) days prior to the filing of the ation disclosure statement;				
•		was cit	ch item of information contained in this Information Disclosure Statement ted in a communication from a foreign patent office in a counterpart application not more than three (3) months prior to the filing of the ent; or				
		was cir foreign after m	item of information contained in the Information Disclosure Statement ted in a communication from a foreign patent office in a counterpart application or, to the knowledge of the person signing the certification taking reasonable inquiry, was known to any individual designated in §) more than three (3) months prior to the filing of the Statement.				
3.			eration of the following additional information (including any co-pending adoned U.S. applications, prior uses and/or sales, etc.) is requested:				
4.	For eac	h non-E	inglish language reference listed on the attached Form PTO-1449:				
		referen	ce is made to an English language translation submitted herewith, and/or				
			ce is made to a foreign patent office search report (in the English ge) submitted herewith, and/or				
			ce is made to an English language translation of a foreign patent office report submitted herewith, and/or				
			ce is made to the concise explanation contained in the specification of the application at page(s), and/or				
		referen	ce is made to the concise explanation set forth below:				

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5.	Applicant also offers the following comments for the Examiner's consideration:
6.	Also enclosed is a copy of a foreign search report citing these references.
7.	The listed documents were brought to the attention of the Applicant(s) after payment of the issue fee in the captioned case. The documents were cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement. Applicant(s) request this Information Disclosure Statement and attached Form PTO-1449 be placed in the file of the captioned application.
8.	Applicant(s) requests that the Information Disclosure Statement and attached Form PTO-1449 and references, which are being filed before the grant of the patent and pursuant to 37 C.F.R. § 1.97(i), be placed in the file of the captioned application.

If any required fees are missing, the Commissioner is authorized to charge said fees to Meyertons, Hood, Kivlin, Kowert & Goetzel, P.C. Deposit Account No. 501505/5681-67300/LJM.

Respectfully submitted,

Lawrence J. Merkel Reg. No. 41,191

Attorney for Applicant(s)

MEYERTONS, HOOD, KIVLIN, KOWERT & GOETZEL, P.C. P. O. Box 398 Austin, Texas 78767 (512) 853-8800

Date: April 21, 2006

Form PTO-1449 (modified)

List of Patents and Publications For Applicant's Information

Disclosure Statement 27 206 (Use several sheets if necessary)

ATTY. DKT. NO. 5681-67300

APPLICANT: Robert E. Cypher

FILING DATE: October 30, 2003

SERIAL NO. 10/696,891

GROUP: 2131

BADENUS. PATENT DOCUMENTS

EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE II APPROPRIATE
		2004/0054956	3/18/04	James Byrd		1.17	
		5,303,302	4/94	Michael Burrows			_
		5,177,747	1/93	Capps, et al.	. 1		
		5,537,421	7/96	Dujari, et al.			
· · · · · · · · · · · · · · · · · · ·		5,606,662	2/97	Michael T. Wisor			
		6,779,150	8/04	Walton, et al.			
· · ·		6,766,493	7/20/04	Hoffman, et al.			
April 1		6,738,947	5/18/04	Maeda			
		6,704,838	3/9/04	Anderson			
		6,374,383	4/16/02	Weng			
		4,433,388	2/21/84	Oosterbaan			
		5,020,060	5/28/91	Murai, et al.			
		4,949,342	8/90	Shimbo, et al.			
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DATE CONSIDERED:

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the patent owner.

Form PTO-1449 (modified) SERIAL NO. 10/696,891 ATTY. DKT. NO. 5681-67300 List of Patents and Publications Use several sheets if necessary 19 27 GROUP: 2131 APPLICANT: Robert E. Cypher For Applicant's Information FILING DATE: October 30, 2003 WATER DOCUMENTS FILING DATE IF **CLASS** SUB REF. DOCUMENT NUMBER DATE NAME EXAM. APPROPRIATE **CLASS** DES. **INITIALS** OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.) J. Zweig, et al., "TCP Alternate Checksum Options," March 1990, 5 pages. F1 Craig Partridge, et al., "Performance of Checksums and CRCs over Real Data," 1995 ACM, pp. 68-76. F2 Jonathan Stone, et al., "Performance of Checksums and CRC's over Real Data, 1998 IEEE, ACM Transaction F3 on Networking, Vol. 6, No. 5, pp. 529-543. Anh Vu, "Error Detection and Error Correction," October 27, 1997, 7 pages. F4 Anastase Nakassis, "Fletcher's Error Detection Algorithm: How to Implement it Efficiently and How to Avoid F5 the Most Common Pitfalls," ACM SIGCOMM Computer Communication Review, Vol. 18, Issue 5, October 1998, pp. 63-88 Anarchriz, "CRC and How to Reverse it: A CRC Tutorial & The c001 Way to Reverse CRC," April 1999, 14 F6 "Cyclic Redundancy Check," August 2, 1997, 1 pages. F7 Eric Smith, "Memory Error Detection and Correction: An Introduction to Parity and ECC," 8/23/96, 2 pages. F8 Timothy Hsu, "How to Select System Memory," January 9, 1999, 13 pages. F9 "Memory Tutorial," 1998, 3 pages F10 Satran, Sheinwald Informational, "iSCSI CRC Considerations," May 7, 2001, 18 pages. F11 "A Short Spiel on 1-Bit Error Correction Codes," printed 8/11/01, 2 pages. F12 Rick Kelly, "Error Management Using DesignWare CRC and ECC," 2001 Synopsys, Inc., 5 pages. F13 ZDNet UK, "Parity RAM and ECC," PC Magazine Guide to Servers, 1999, 1 page. F14 F15 "Before you start - Chapter 6: Break and Verify," © Laurence Soucy 1997/2001, 1 page. F16 "A Painless Guide to CRC Error Dection Algorithms," Version 3.00, 9/24/96, 19 pages. "Error Control Systems for Digital Communication and Storage," Stephen B. Wicker, Prentice Hall 1995, F17

EXAMINER:

pages. 100-123.

DATE CONSIDERED:

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the patent owner.